

# SSD Protocol Decodes for PCI Express<sup>®</sup> 3.0

# SSD Decodes Speed Protocol Testing and Product Development!

## **Key Features**

- See and understand the traffic
  - Get useful information quickly
  - · More choices of data views
  - · More ways to analyze data

#### Find errors fast

- Displays show specific decodes of SSD protocol traffic
- Easy-to-understand traffic displays at multiple layers of the protocol
- Large trace memory
- Powerful triggering/filtering

#### Accurate data capture

- 100% data capture of all PCI Express traffic
- Supports all SSD data rates for PCI Express
- The most extensive suite of SSD Protocol Decodes in the PCle Protocol Test industry
  - Provides engineers more information quickly to solve problems, reduce development expenses and bring products to market quickly



Teledyne LeCroy's PCIe Protocol Suite supports an extensive library of protocol decodes for virtually all PCIe® applications. These decodes enable Teledyne LeCroy analyzers to display data traffic with meaningful and easy-to-understand displays at multiple levels in the protocol stack, and all protocol decode libraries are included as standard features of every Teledyne LeCroy PCI Express protocol analyzer.



Key features for PCIe Solid-state Device (SSD) developers include libraries of decodes specifically designed for SSD protocols, such as NVM Express, SCSI Express and SATA Express. Teledyne LeCroy rapidly implements changes and improvements in specifications as they are approved. For details on current protocols supported, see the PCIe Protocol Suite Readme file in the Software Downloads section of Support on teledynelecroy.com.

#### **NVM Express**

NVM Express (NVMe) is sponsored by the NVM Express Work Group. The NVM Express specification defines an optimized register interface, command set and feature set for PCI Express Solid-State Drives (SSDs). The specification defines a native PCI Express interface that streamlines the interface between host controllers and SSD/drive devices, increasing efficiency and performance in NVM Express devices for Enterprise and Client systems.

#### **SCSI Express**

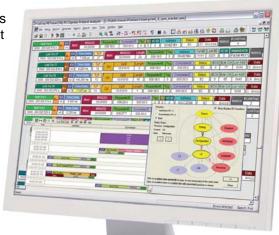
The SCSI Express protocol is sponsored by the INCITS T10's SOP-PQI Working group and the SCSI Trade Association. The protocol is based on the SCSI over PCIe (SOP) host interface specification which enables SCSI initiators communicating to SCSI targets over PCIe through the PQI transfer layer. This new protocol will enable SCSI devices to utilize the faster PCIe transport required to meet the demand in next generation enterprise designs.

#### **SATA Express**

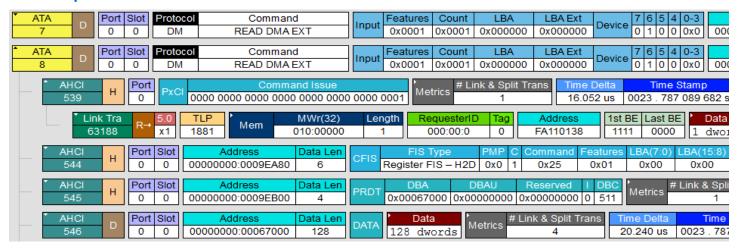
The SATA Express protocol is sponsored by the Serial ATA International Organization (SATA-IO). This protocol combines the SATA AHCI software

specification with the PCIe host interface. SATA Express enables new devices to be developed that utilize the faster PCIe interface and maintain compatibility with a broad base of existing SATA applications.

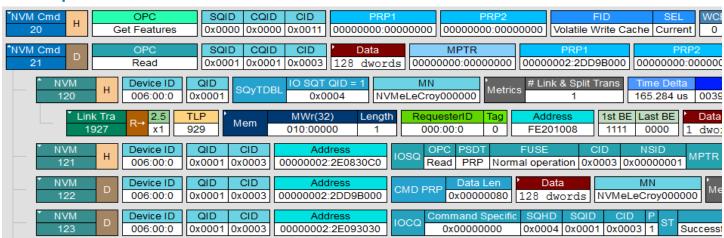
The storage industry is developing these technologies for several reasons, including faster data access, increased longevity and reliability, less noise, non-volatile storage, and less maintenance of failing hard drives.



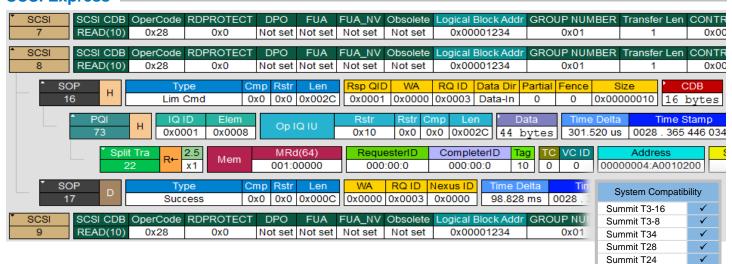
### SATA Express



#### **NVM Express**



### **SCSI Express**





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